

**Amendments to the Specification:**

Please amend the specification as follows:

Please replace the first paragraph on page 3, beginning with “In the semiconductor,” and ending with “powered on,” with the following replacement paragraph:

--In the semiconductor integrated circuit as described above, the nonvolatile memory unit comprised of a nonvolatile memory such as an electrically erasable programmable read-only memory (“EEPROM”) stores therein the coded license code, and the decoder circuit decodes the license code, thereby making it possible to make each function block either usable or unusable depending on the license code. This eliminates the need to enter license codes each time the circuit is powered on.—

Please replace the first paragraph on page 11, beginning with “A license code,” and ending with “license codes,” with the following replacement paragraph:

--A license code indicating that the number of valid use of the function block 17 is n, for example, is entered from the exterior of the semiconductor integrated circuit 10, and is stored in the nonvolatile memory unit 11 comprised of EEPROM. For example, such license information may be written into the semiconductor integrated circuit 10 by use of [[an]] large scale integrated circuit (“LSI”) tester or the like at the time of shipping out from the factory. If this is the way it is done, there is no need to provide the package of the semiconductor integrated circuit with external pins for the purpose of writing license codes.--